

FIG. 1

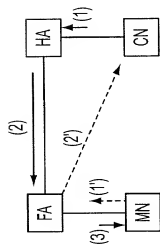


FIG. 2A

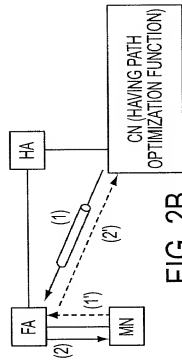


FIG. 2B

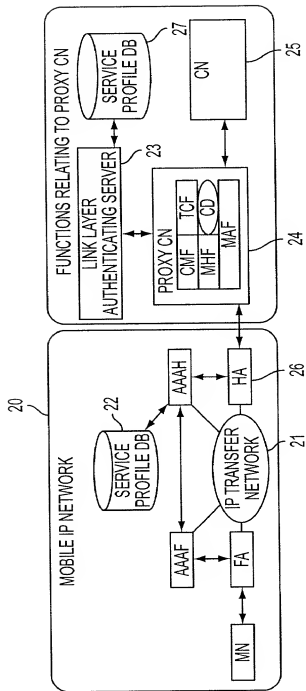


FIG. 3

CONSTITUENT INFORMATION	DETAILED CONSTITUENT INFORMATION	DESCRIPTION	
		HOME ADDRESS OF MOBILE NODE, ETC.	
CONTROL TARGET PACKET INFORMATION	TRANSMISSION SOURCE ADDRESS		
	TRANSMISSION SOURCE PORT NO.	PORT NUMBER IN IP HEADER	
	TRANSMISSION DESTINATION ADDRESS	RECEPTION DESTINATION ADDRESS IN IP HEADER	
	TRANSMISSION DESTINATION PORT NO.	RECEPTION DESTINATION PORT NO. IN IP HEADER	
ROUTING/PACKET EDITING INFORMATION	ENCAPSULATING(ENCRYPTING) METHOD		
	TRANSFER DESTINATION ADDRESS	TRANSMISSION DESTINATION ADDRESS AS A RESULT OF CONTROL	
	TOS (TYPE OF SERVICE) VALUE OF IP	SERVICE TYPE	
	DE-ENCAPSULATING INSTRUCTION		
INDIVIDUAL CONTROL INFORMATION	NEXT SERVICE CONTROL TYPE	INFORMATION FOR APPLICATION OF IP TRAFFIC CONTROL SUCH AS DIFF-SERV, ETC.	

FIG. 4

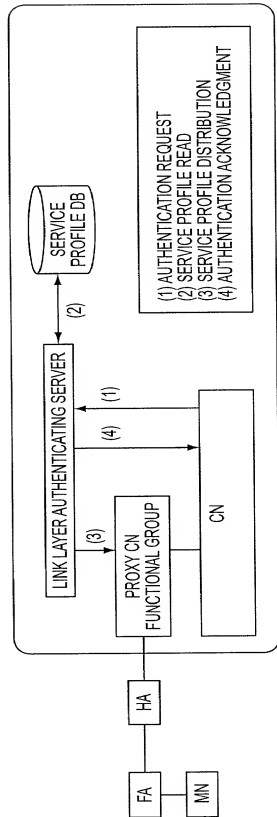


FIG. 5

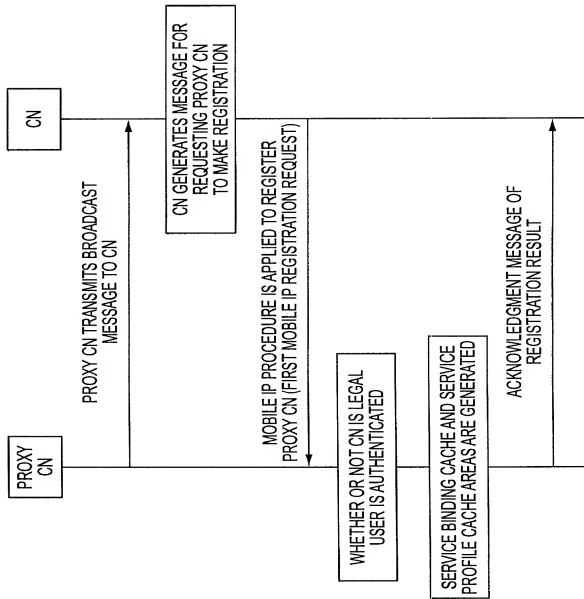


FIG. 6

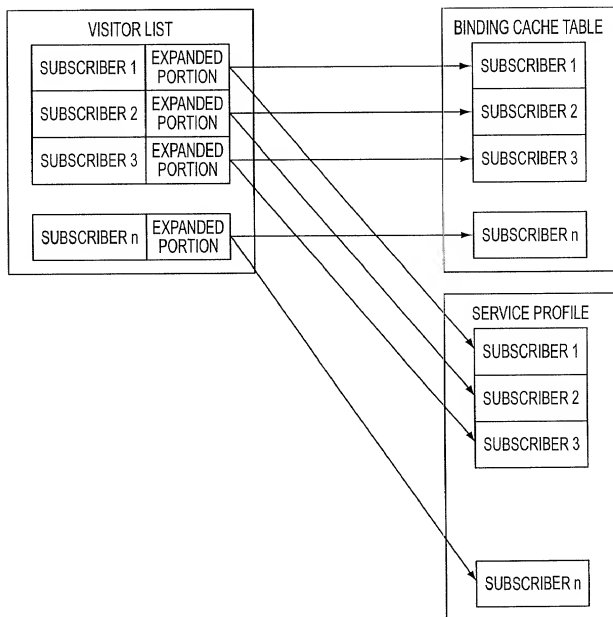


FIG. 7

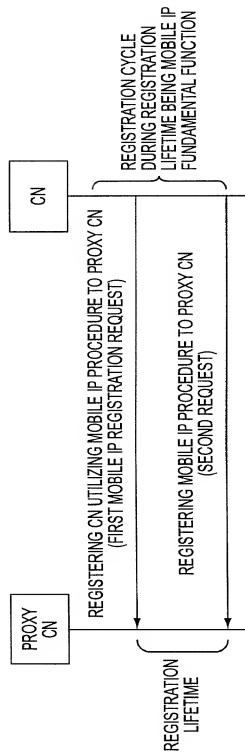


FIG. 8

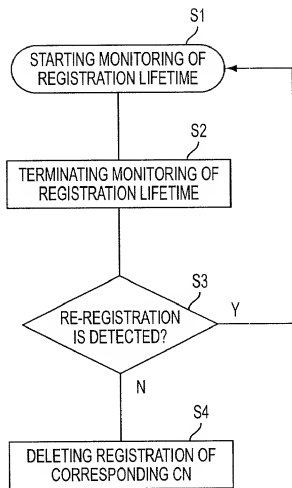


FIG. 9

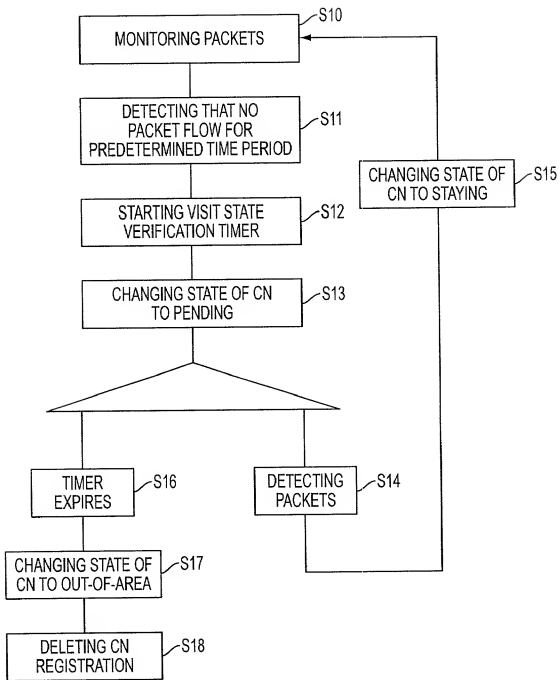


FIG. 10

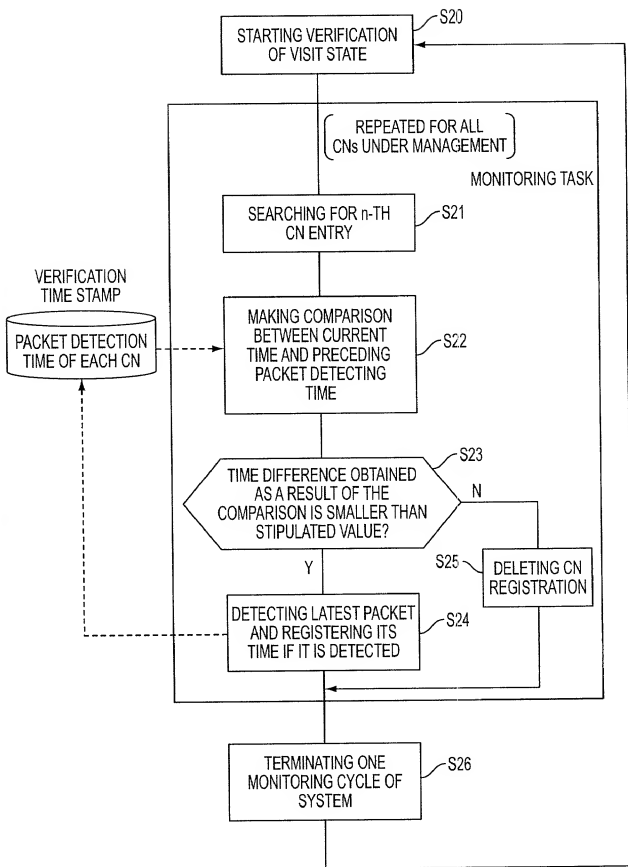


FIG. 11

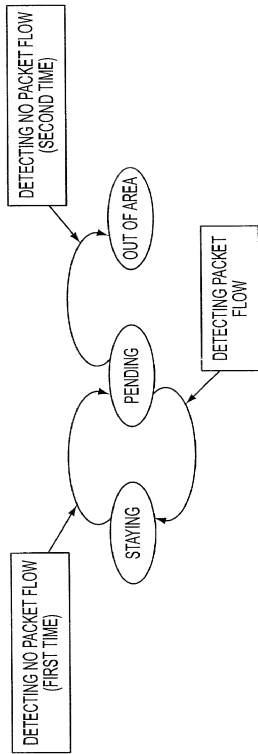


FIG. 12

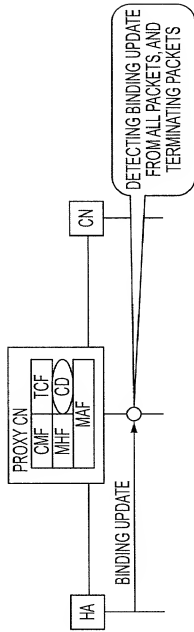


FIG. 13

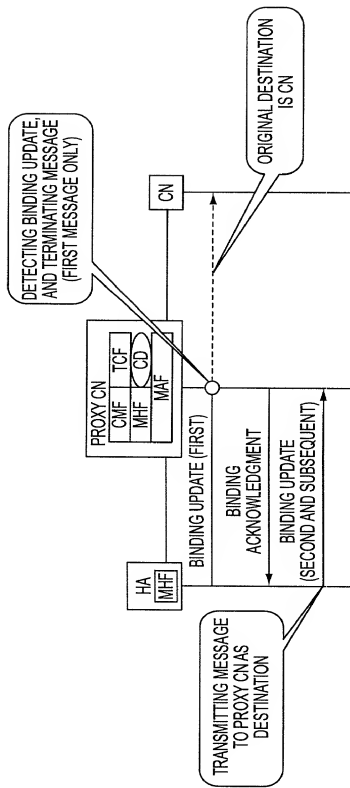


FIG. 14

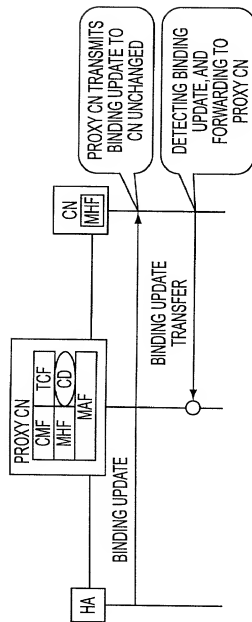


FIG. 15

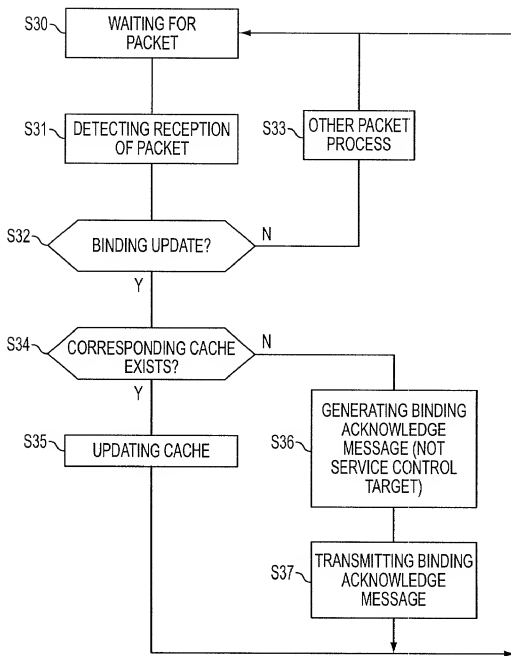


FIG. 16


```
graph TD
    S40[WAITING FOR PACKET] --> S41[DETECTING RECEPTION OF PACKET]
    S41 --> S42{BINDING UPDATE?}
    S42 -- Y --> S44{CORRESPONDING CACHE EXISTS?}
    S42 -- N --> S43[OTHER PACKET PROCESS]
    S43 --> S40
    S44 -- Y --> S45[UPDATING CACHE]
    S44 -- N --> S46[GENERATING CACHE]
    S45 --> Exit(( ))
    S46 --> Exit
```

The flowchart illustrates the packet processing method. It begins with a process block S40 labeled "WAITING FOR PACKET". An arrow leads down to process block S41 labeled "DETECTING RECEPTION OF PACKET". From S41, an arrow leads down to decision diamond S42 labeled "BINDING UPDATE?". From S42, a "Y" path leads down to decision diamond S44 labeled "CORRESPONDING CACHE EXISTS?". From S42, an "N" path leads right to process block S43 labeled "OTHER PACKET PROCESS". From S43, an arrow leads up and left to join the arrow entering S40. From S44, a "Y" path leads down to process block S45 labeled "UPDATING CACHE". From S44, an "N" path leads right to process block S46 labeled "GENERATING CACHE". Both S45 and S46 have arrows leading down to a common exit point at the bottom of the diagram.

FIG. 17

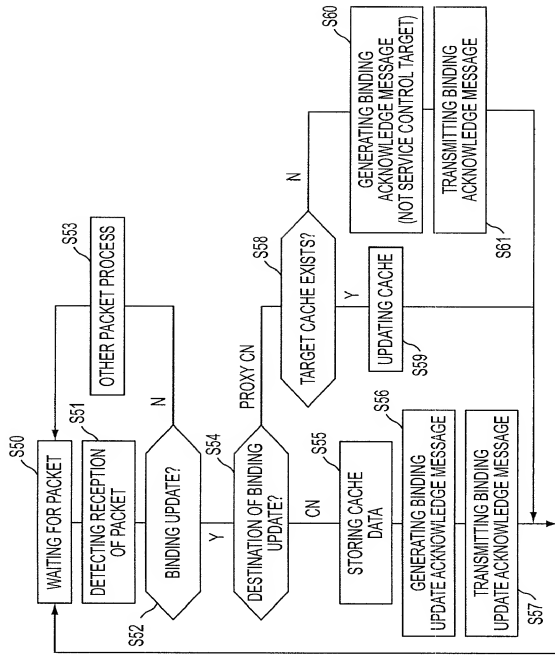


FIG. 18

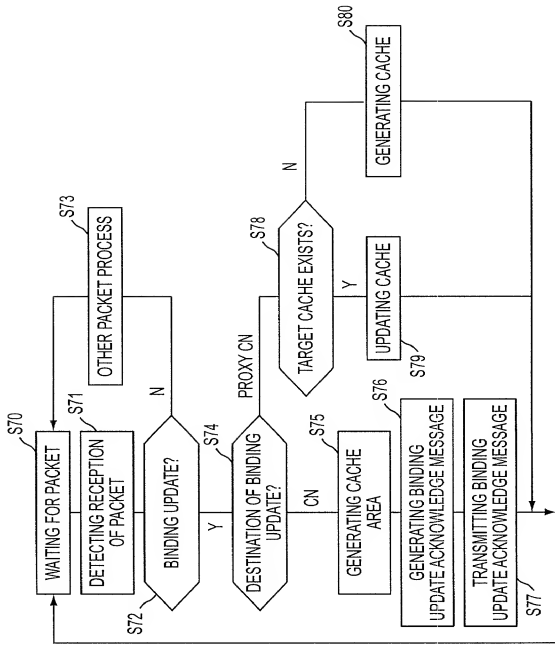


FIG. 19

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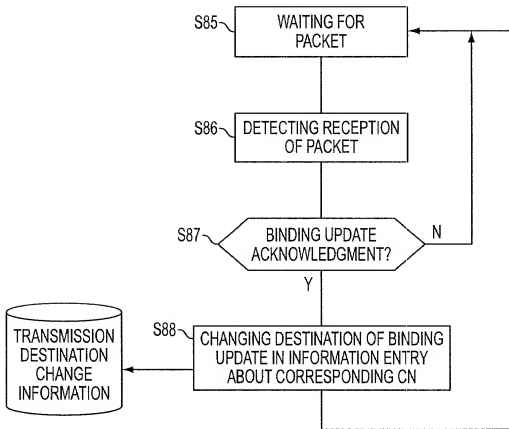


FIG. 20

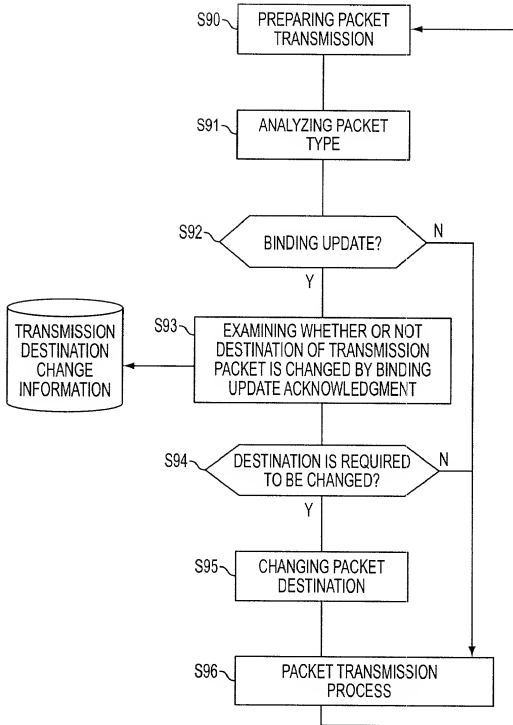


FIG. 21

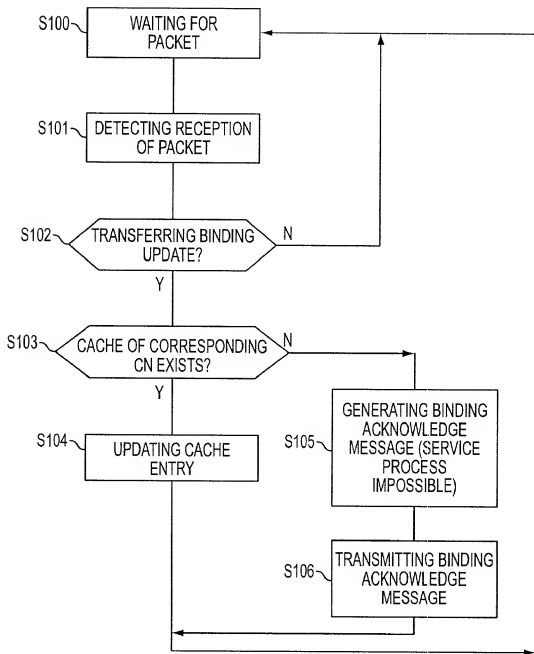


FIG. 22

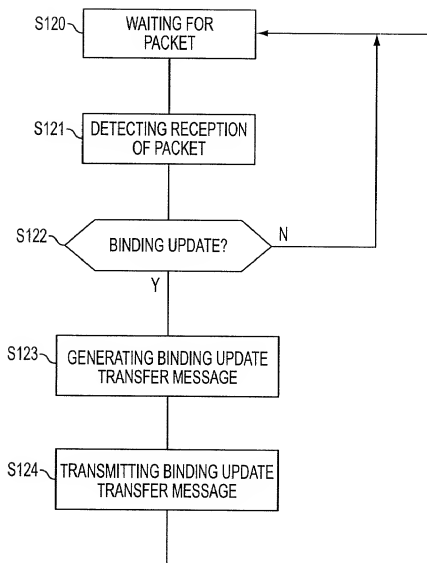
[illegible]

FIG. 24

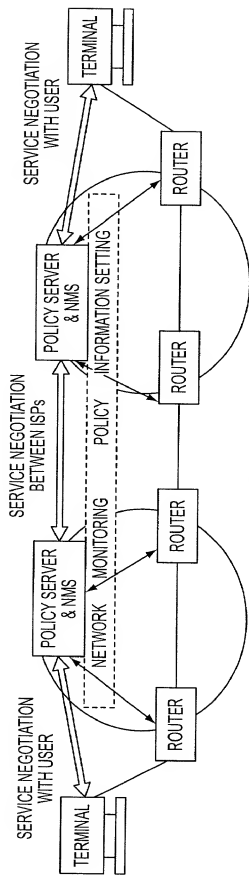


FIG. 25